IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): TANAKA et al

Serial No.:

09/942,213

Filed:

4

August 30, 2001

For:

Method And Apparatus For Inspecting A Semiconductor Device

Group:

2812

Examiner:

PROPOSED AMENDMENTS TO THE DRAWINGS

Assistant Commissioner for Patents Washington, DC 20231

December 27, 2001

Sir:

It is proposed that the drawings in the above-identified application be amended in accordance with the attached red-lined prints, and approval of these drawing corrections is respectfully requested at this time.

Upon receipt of the approval of the amendments to the drawings and receipt of the official Notice of Allowance, the drawing amendments will be effected.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

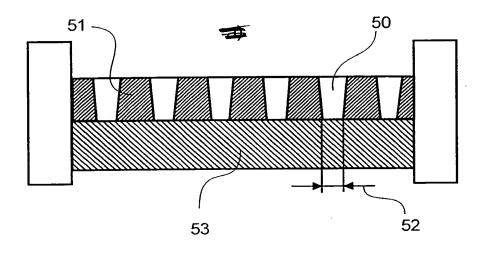
Alan E. Schiavelli

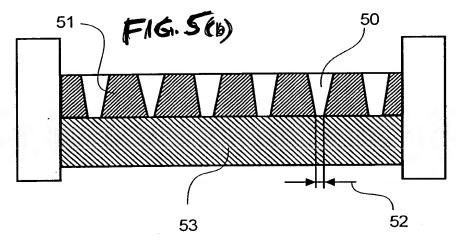
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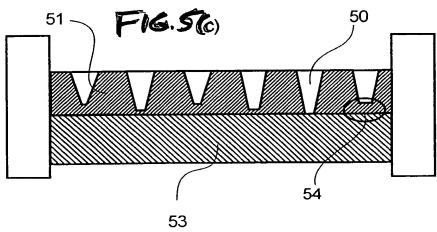
DRA/AES/jla (703) 312-6600 Attachments



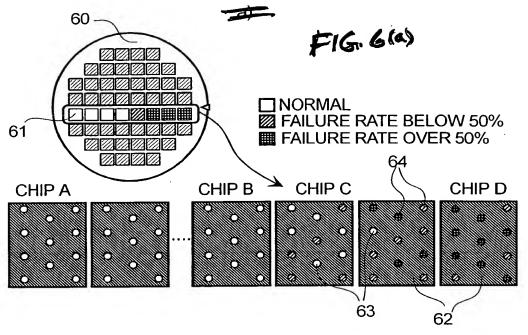
FIG.5(A)

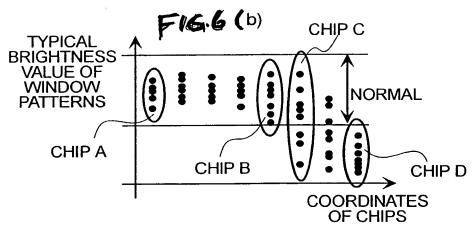


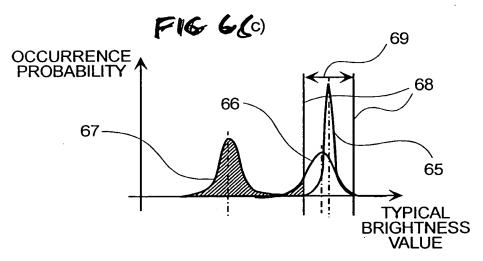














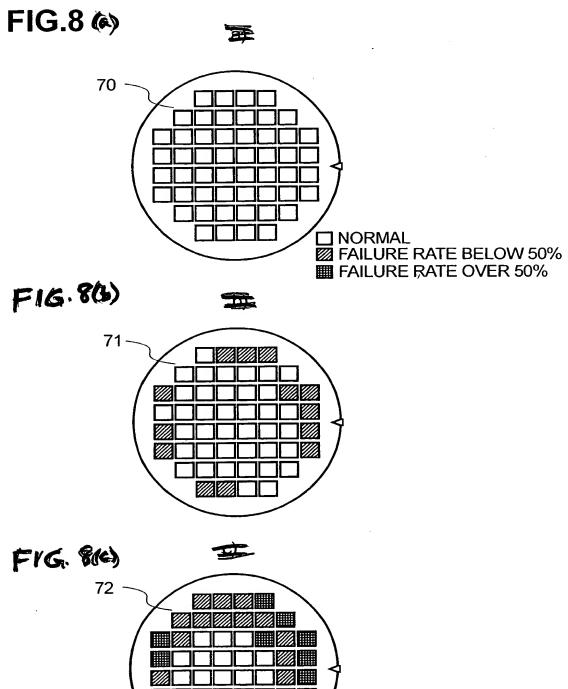




FIG.9(a)

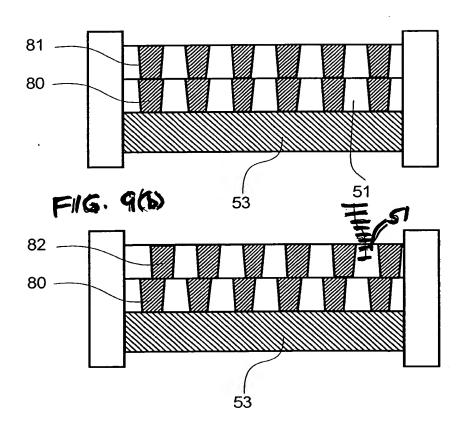


FIG.10(4)

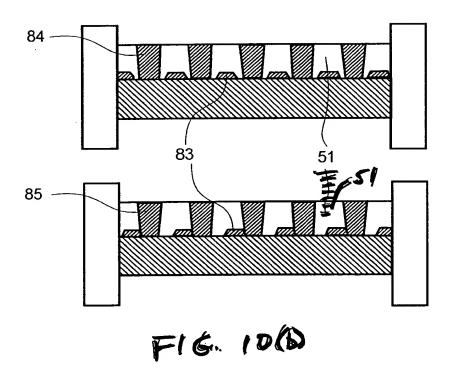


FIG.12 (4)

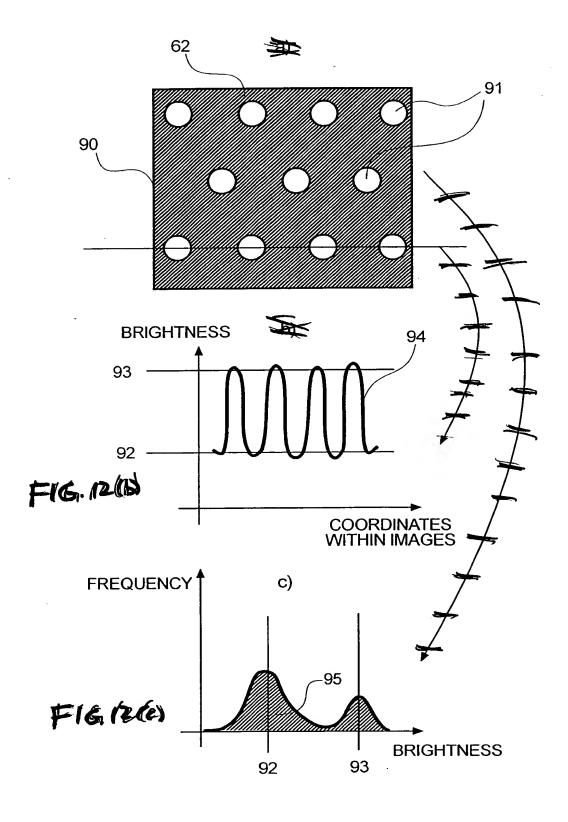
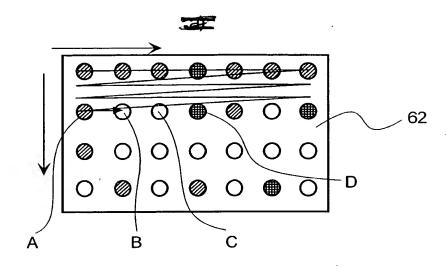
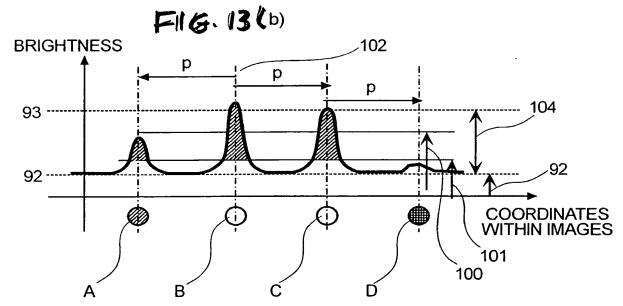


FIG.13(a)





BRIGHTNESS

101

COORDINATES
WITHIN IMAGES

EIG.14

FIG. 14(a) LOW PATTERN DENSITY

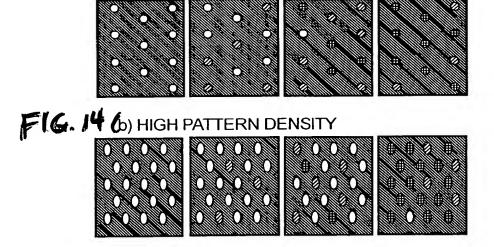
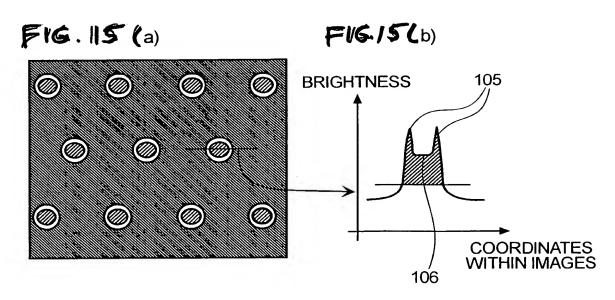
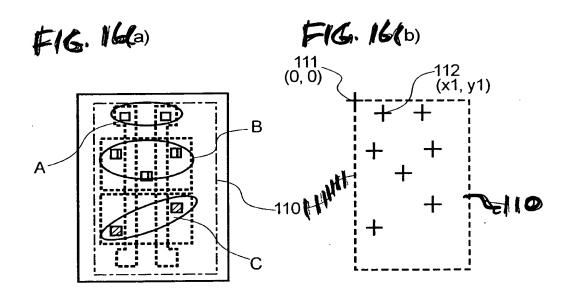
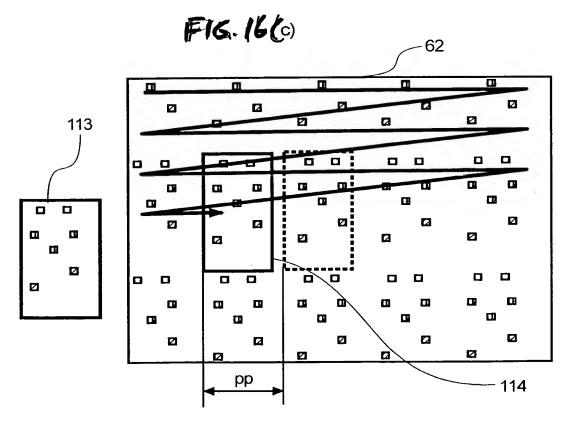


FIG. 15



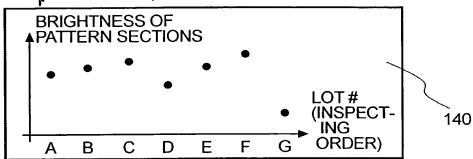
EIG.16



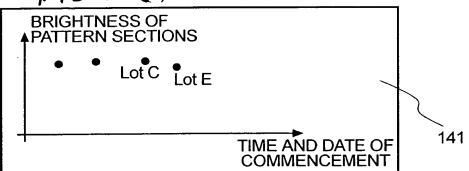


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FIG. 24 (a)



F16.21 (b)



F16.21 (c)

